

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**IN THE CLAIMS**

1. (Previously Presented) A voltage regulation apparatus for use on an integrated circuit device, comprising:

an automatic process identifier configured to identify a process split of the device;

a memory circuit coupled to the automatic process identifier, the memory circuit configured to store data comprising target voltages for different process splits;

a processing unit;

a power supply; and

a voltage regulator circuit coupled to the memory circuit and to the power supply, the regulator configured to adjust the power supply value according to the automatic process identifier and the memory circuit, the adjusted power supply causing the processing unit to operate substantially at a target frequency.

2. (Previously Presented) The apparatus of claim 1, further comprising:

a switching device comprising a plurality of inputs and an output, a first one of the inputs of the plurality coupled to the memory circuit and the output coupled to the voltage regulator; and

a closed-loop feedback circuit coupled to a second one of the inputs of the plurality, the feedback circuit configured to adjust the power supply voltage.

3. (Previously Presented) The apparatus of claim 2, wherein the closed-loop feedback circuit comprises a critical path replica circuit configured to approximate a critical path of the processing unit.

4. (Previously Presented) The apparatus of claim 2, wherein the switching device is configured to select between adjusting the power supply based upon the automatic process identifier and the closed-loop feedback circuit.

5. (Previously Presented) The apparatus of claim 3, wherein the closed-loop feedback circuit further comprises a counter for measuring an output frequency of the critical path replica.

6. (Previously Presented) The apparatus of claim 1, wherein the memory circuit contains data comprising a plurality of target frequencies, the target frequencies corresponding respectively to each of the target voltages.

7. (Previously Presented) The apparatus of claim 5, wherein the memory circuit contains data comprising a plurality of target frequencies, the target frequencies corresponding respectively to each of the target voltages.

8. (Previously Presented) An integrated circuit device comprising:  
a processing unit;  
process identification means for identifying the process split of the integrated circuit;  
memory means for storing characterization data of the family of integrated circuit devices to which the integrated circuit device belongs;  
means for determining the characterization data for the integrated circuit device using the memory means and the process identification means; and  
voltage regulation means for adjusting the supply voltage using the characterization data for the integrated circuit device to achieve a desired target frequency of operation for the processing unit.

9. (Previously Presented) The integrated circuit device of claim 8, wherein the memory means includes characterization data for worst case temperature variation for each process split.

10. (New) A method of dynamic voltage scaling, comprising:  
identifying the process split of an integrated circuit;  
storing characterization data of a family of integrated circuit devices to which the integrated circuit device belongs;  
determining the characterization data for the integrated circuit device using the stored characterization data of the family of integrated circuits devices and the identified process split of an integrated circuit; and

adjusting the supply voltage using the characterization data for the integrated circuit device to achieve a desired target frequency of operation for the processing unit.

11. (New) The method of claim 10, wherein the storing of characterization data includes storing characterization data for worst case temperature variation for each process split.

12. (New) A computer program product for dynamic voltage scaling, comprising:  
computer readable medium comprising the following code:  
code for causing a computer to identify the process split of an integrated circuit;

code for causing a computer to store characterization data of the family of integrated circuit devices to which the integrated circuit device belongs;

code for causing a computer to determine the characterization data for the integrated circuit device using the stored characterization data of the family of integrated circuits devices and the identified process split of an integrated circuit; and

code for causing a computer to adjust the supply voltage using the characterization data for the integrated circuit device to achieve a desired target frequency of operation for the processing unit.

13. (New) A computer program product of claim 12, wherein the code for causing a computer to store characterization data includes code for storing characterization data for worst case temperature variation for each process split.